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PATENT
Customer No. 22,852
Attorney Docket No. 08245.0027

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
)
Jun Dong KIM et al.) Group Art Unit: 2814
)
Application No.: 09/892,878) Examiner: Shrinivas H. Rao
)
Filed: June 28, 2001)
)
For: METHOD FOR FORMING GATE)
ELECTRODES IN A)
SEMICONDUCTOR DEVICE USING)
FORMED FINE PATTERNS)

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

PETITION FOR EXTENSION OF TIME

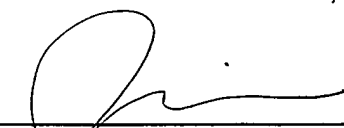
Applicants hereby petition for a two-month extension of time to reply to the Office
Action of July 28, 2003. A fee of \$420.00 is enclosed.

Please grant any extensions of time required to enter this response and charge any
additional required fees to our deposit account 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

Dated: December 11, 2003

By: 
Qingyu Yin*

*With limited recognition under 37 C.F.R. §10.9(b).

FINNEGAN
HENDERSON
FARABOW
GARRETT &
DUNNER LLP

1300 I Street, NW
Washington, DC 20005
202.408.4000
Fax 202.408.4400
www.finnegan.com

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